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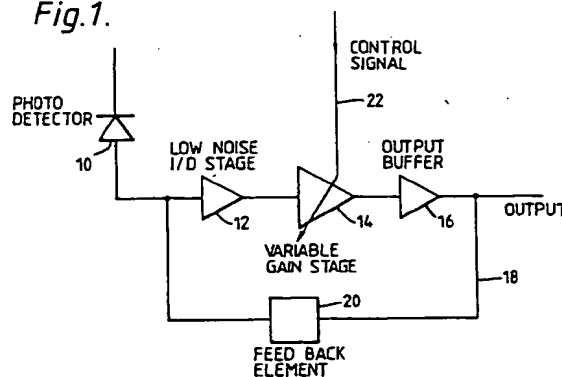
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(54) **HF amplifier bandwidth control.**

(57) A method of controlling the bandwidth in a high frequency transimpedance feedback amplifier comprising the step of applying a d.c. control current to control the forward gain of a voltage amplifying transistor stage in the amplifier the gain of which amplifying stage is dependent on the collector current of a transistor in the stage.

Fig.1.



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thereby to control the amount of gain in the feedback loop (18,20) and hence the band-width of the amplifier.

4. An amplifier according to claim 3 characterised in that the voltage amplifying stage (14) comprises first and second transistors (TR2, TR3) connected in a Darlington pair configuration and the d.c. control current (22) is applied to the collectors of the pair.

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EUROPEAN SEARCH REPORT

Application Number

EP 92 30 9117

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
X	US-A-3 436 670 (J. SOLOMON) * the whole document *	1-3	H03G5/28
P, X	PATENT ABSTRACTS OF JAPAN vol. 016, no. 088 (E-1173)2 December 1991 & JP-A-32 70 504 (FUJITSU LTD) 4 March 1992 * abstract *	1	
			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H03G
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 17 FEBRUARY 1993	Examiner DECONINCK E.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application I : document cited for other reasons & : member of the same patent family, corresponding document			